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PATENT APPLICATION
DOCKET NO.: MOS91-02AF

PATENT & TRADEMARK
OFFICE
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
MAY -2 97

Applicant PATENT MAINTENANCE Richard C. Foss, Peter B. Gillingham, Robert
DIVISION F. Harland, Masami Mitsuhashi and Atsushi Wada

Patent No.: 5,414,662

Issued: May 9, 1995

Title: DYNAMIC RANDOM ACCESS MEMORY USING IMPERFECT
ISOLATING TRANSISTORS

16.05 APR 30 10:36
OFFICE OF THE
PATENT AND TRADEMARK
OFFICE

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to Honorable Commissioner of Patents and Trademarks, Washington D.C. 20231	
on <u>4-30-97</u>	<u>Claire J. Harland</u>
Date	Signature
<u>Claire J. Harland</u>	
Typed or printed name of person signing certificate	

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REQUEST FOR CHANGE OF DOCKET NUMBER

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Honorable Commissioner of
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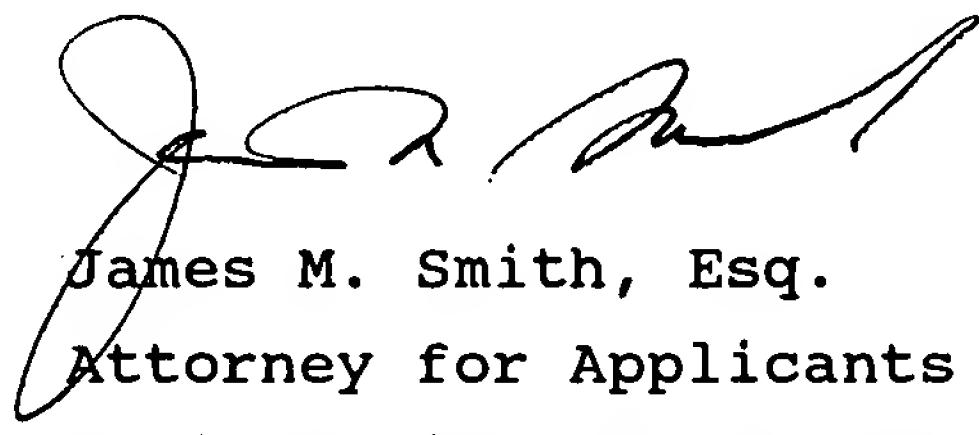
Sir:

Applicant's attorney requests that the docket number
of record for the above-referenced case be changed to
MOS91-02AF. This request is made to better identify this
application within our offices.

07/14/1997 R.JOHNSON 000000091 5414662 25.00 0P
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A check in the amount of \$25.00 is enclosed. Please charge any additional fees that may be due in this matter to Attorney's Deposit Account No. 08-0380. Two duplicate copies of this letter are enclosed for accounting purposes.

Respectfully submitted,



James M. Smith, Esq.
Attorney for Applicants
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Lexington, Massachusetts 02173

Date: 4/24/97